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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TESHIROGI, Kazuo et al.**

Group Art Unit: **2818**

Serial No.: **10/708,067**

Examiner: **LE, Thao P.**

Filed: **February 6, 2004**

P.T.O. Confirmation No.: 2066

For: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE AND
A METHOD FOR FIXING THE SEMICONDUCTOR DEVICE USING
SUBSTRATE JIG**

RESPONSE TO THE RESTRICTION REQUIREMENT
DATED JULY 9, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: July 27, 2004

Sir:

This paper is submitted in response to the Official Action dated **July 9, 2004**.

In the Action, restriction is required between Group (I), Claims 1-3; Group (II), Claim 4;
Group (III), Claim 5 and Group (IV), Claim 6.

Applicant hereby elects the subject matter of Group (III), Claim 5 for prosecution in this application. This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please
charge our Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE